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**Art Unit 2117**

**Attn: Mail Stop Amendment**

Re: U.S. Utility Patent Application  
Application No. 10/681,244; Filed: October 9, 2003  
For: **Integrated Packet Bit Error Rate Tester for 10G SERDES**  
Inventors: Baumer *et al.*  
Our Ref: 1875.4430000

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Information Disclosure Statement;
2. Form PTO/SB/08A (2 sheets) listing twenty-two (22) documents (US1-US2);
3. Form PTO/SB/08B (2 sheets) listing twenty-five (25) documents (NPL1-NPL25); and
4. Copies of cited documents (NPL1-NPL25).

The above-listed documents are filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

Commissioner for Patents  
October 31, 2007  
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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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JTH/ahw  
Enclosures

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